

REMARKS

The Office Action objects to the title of the invention as not descriptive. The title has been amended to "ROW DRIVER FOR SELECTIVELY SUPPLYING OPERATING POWER TO IMAGER PIXEL".

Claims 1-31 are pending. Claims 8-18 and 21-31 are withdrawn from consideration. Claims 19-20 are allowed. Claims 1-7 stand rejected. Claims 1 and 7 are currently amended.

Claims 1-6 stand rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 6,184,055 to Yang. This rejection is respectfully traversed.

Claim 1 recites a method of operating a pixel of an image sensor comprising, inter alia, "selectively supplying operating power to said pixel to produce an output signal on an output line based on the charge at said storage node, said selectively supplying step comprising selectively providing operating power to a channel terminal of a reset transistor configured to reset said storage node and to a first channel terminal of a source follower transistor having a gate coupled to said storage node, said source follower transistor having a second channel terminal coupled to said output line."

Lee does not disclose these features. The Office Action admits at page 6 that "Prior art does not disclose . . . turning on the reset transistor . . . ; providing a high signal to the channel terminal of the reset transistor; . . . providing a low signal to the channel terminal of the reset transistor; [and] turning off the reset transistor." Similarly, Lee does not disclose "selectively providing operating power to a channel terminal of a reset transistor configured to reset said storage node and to a first channel terminal of a source follower transistor having a gate coupled to said storage node, said

source follower transistor having a second channel terminal coupled to said output line."

Accordingly, Applicants respectfully request that the rejection of claim 1 and all dependent claims be withdrawn and the claims allowed.

Claim 7 stands rejected under 35 U.S.C. §103(a) as being unpatentable over Lee in view of U.S. Patent Publication No. 2005/0083421 to Berezin et al. This rejection is respectfully traversed.

The deficiencies of Lee have been discussed above with respect to claim 1, from which claim 7 depends. Berezin also fails to disclose, teach or suggest "selectively providing operating power to a channel terminal of a reset transistor configured to reset said storage node and to a first channel terminal of a source follower transistor having a gate coupled to said storage node, said source follower transistor having a second channel terminal coupled to said output line." Applicants respectfully request that the rejection of claim 7 be withdrawn and the claim allowed.

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In view of the above amendment, applicant believes the pending application is in condition for allowance.

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